Form PTO-1449 (modified)	Attorney Docket No. Intel 2207/670602	Serial No. 10/625,240
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT(S)' INFORMATION DISCLOSURE STATEMENT	**Applicant** Opher D. KAHN et al.	
	Filing Date July 22, 2003	**Group Art Unit** 2183

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE*
ODH	**5,446,912	August 29, 1995	Colwell et al.	395	800	December 29, 1993
ODH	**6,014,739	January 11, 2000	Christie	712	228	October 27, 1997
ODH	**6,157,996	December 5, 2000	Christie	712	218	November 13, 1997
ODH	**6,230,259	May 8, 2001	Christie et al.	712	228	October 31, 1997

**A copy is not enclosed since the present application was filed after June 30, 2003. (*See* OG Dated: August 5, 2003)

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
ODH	Pentium® Pro Family Developer's Manual; Volume 2: Programmer's Reference Manual , pps. 11-1 – 11.3, 1996.
ODH	Intel Architecture Software Developer's Manual; Volume 2: Instruction Set Reference, pps. 2-1 – 2-6, 1997
ODH	Intel Architecture Software Developer's Manual; Volume 1: Basic Architecture, pps. 2-1 – 2-13, 1999
ODH	Intel Architecture Software Developer's Manual; Volume 2: Instruction Set Reference, pps. B-19 – B-34, 1999
ODH	Chia-Lin Yang, Barton Sano, and Alvin S. Lebeck, "Exploiting Instruction Level Parallelism in Geometry Processing for Three Dimensional Graphics Applications", #0-8186-8609-X/98, pps. 14-24, 1998 IEEE

EXAMINER		DATE CONSIDERED	3-1-06

EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.